

ABSTRACT

A common mode signal suppressing circuit comprises: a first winding (11) inserted to a conductor line (3); a second winding (12) that is inserted to a conductor line (4) and coupled to the first winding (11) through a magnetic core (10) and that suppresses common mode signals in cooperation with the first winding (11); and a third winding (13) coupled to the first and second windings (11, 12) through the core (10). The common mode signal suppressing circuit further comprises a phase-inverted signal transmitting circuit (15) connected to the third winding (13) and to the conductor lines (3, 4). The phase-inverted signal transmitting circuit (15) detects a common mode signal on the conductor lines (3, 4), and supplies a phase-inverted signal to the third winding (13), the phase-inverted signal having a phase opposite to the phase of the common mode signal.